

SECOMP: Formally Secure Compilation of Compartmentalized C Programs



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Hiring: PostDoc, interns, PhD students

Joint work with

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Dongjae Lee, Guido Martínez, Marco Patrignani, Benjamin Pierce, Exequiel Rivas,
Marco Stronati, Éric Tanter, Jérémy Thibault, Andrew Tolmach, Théo Winterhalter, ...

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The C programming language is insecure

–any **buffer overflow** can be catastrophic



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- ~100 different **undefined behaviors** in the usual C compiler:
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- ~100 different **undefined behaviors** in the usual C compiler:
 - **use after frees and double frees, invalid type casts, signed integer overflows, concurrency bugs, ...**
- **root cause**, but very challenging to fix:
 - **efficiency**, precision, scalability, backwards compatibility, deployment



Mitigation: compartmentalization



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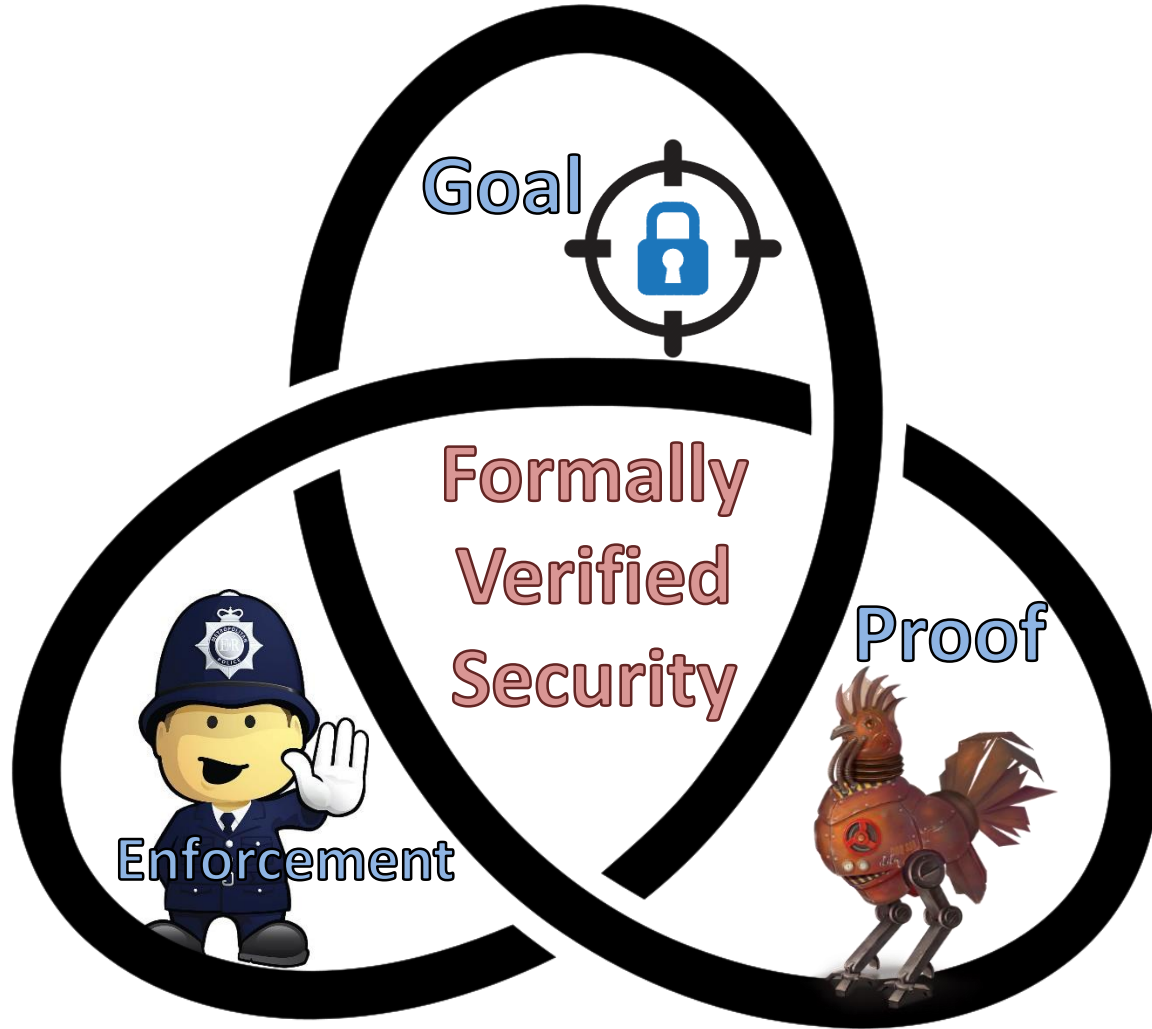
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 - against compartments dynamically compromised by undefined behavior
- **Targeting various enforcement mechanisms**
 - software-fault isolation (SFI), capability machines, ...



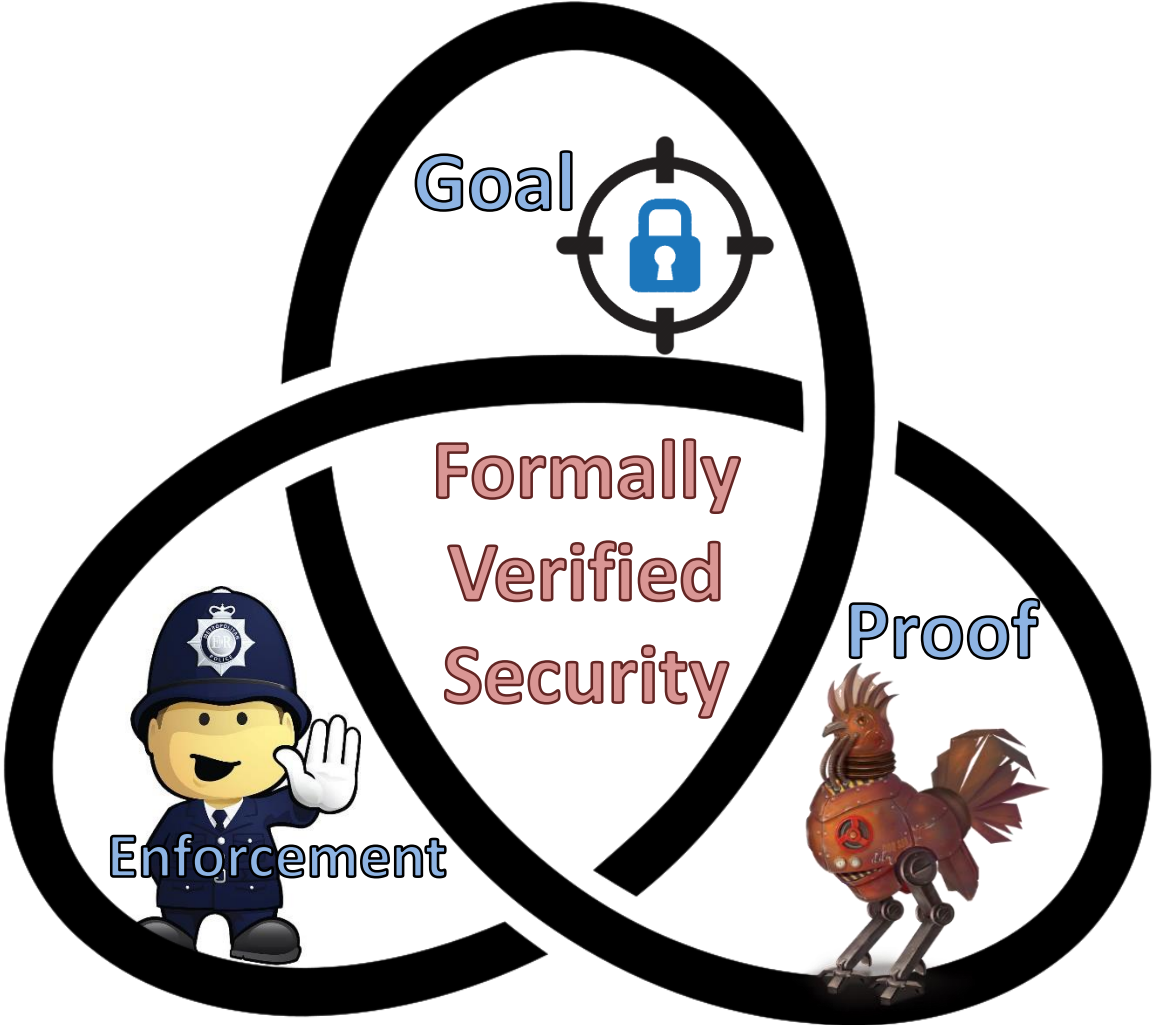
Formally Verified Security







Formally Secure Compilation of C Compartments





1. Security Goal



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- **What does it mean for a compilation chain for vulnerable C compartments to be secure?**



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- What does it mean for a compilation chain for vulnerable C compartments to be secure?
- As a warmup, I will first show an easier definition
 - protecting 1 trusted compartment from 1 untrusted one (arbitrary ASM)
 - trusted compartment has no vulnerabilities, e.g. formally verified
 - e.g. EverCrypt verified crypto library, shipping in Firefox, Linux Kernel, ...
 - e.g. simple verified web server, linked with unverified libraries [POPL'24]
- What does it mean to securely compile such a verified compartment against linked adversarial target-level code?



Preserving security **against adversarial contexts**



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\forall security property π



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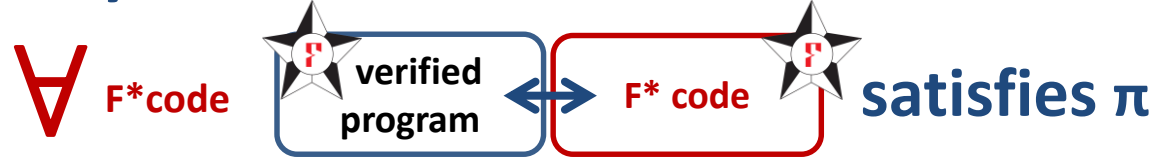


satisfies π



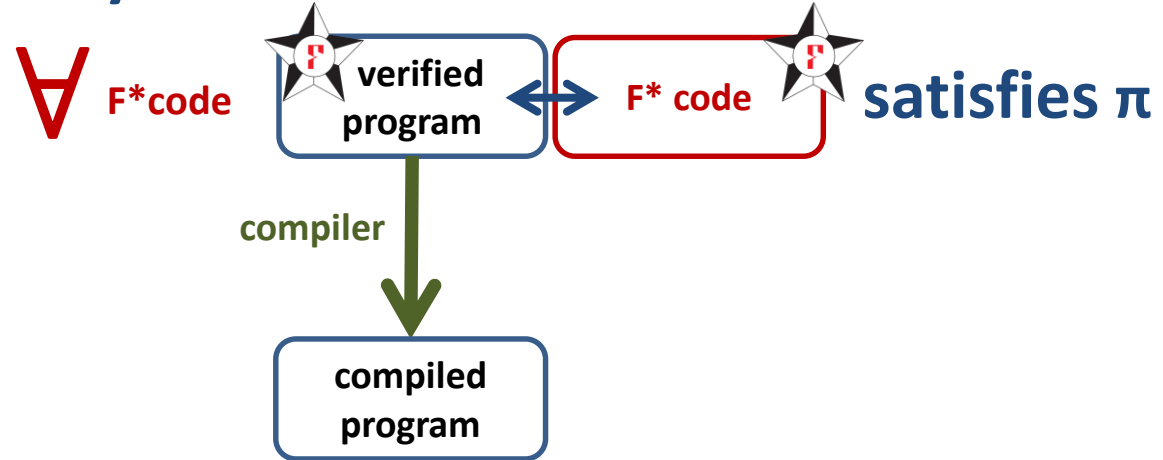
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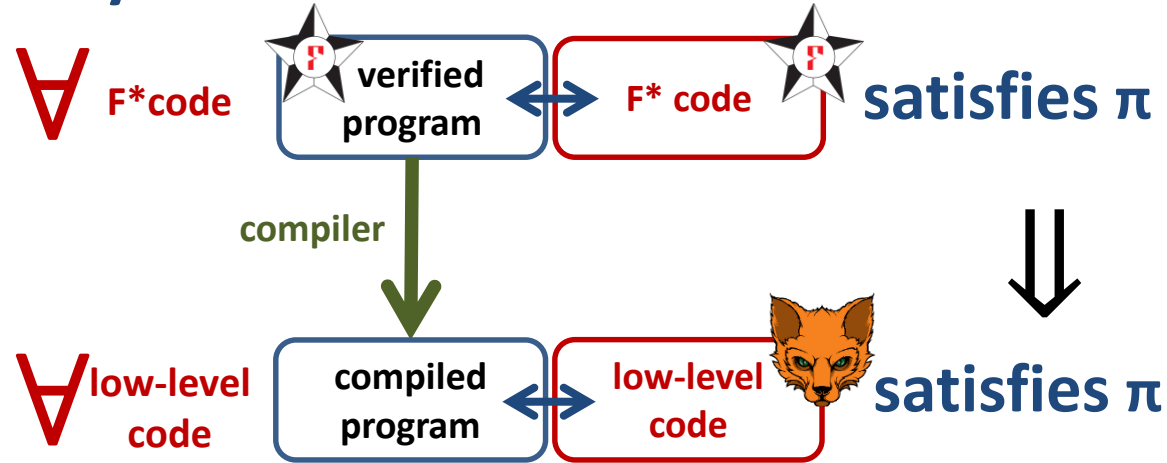
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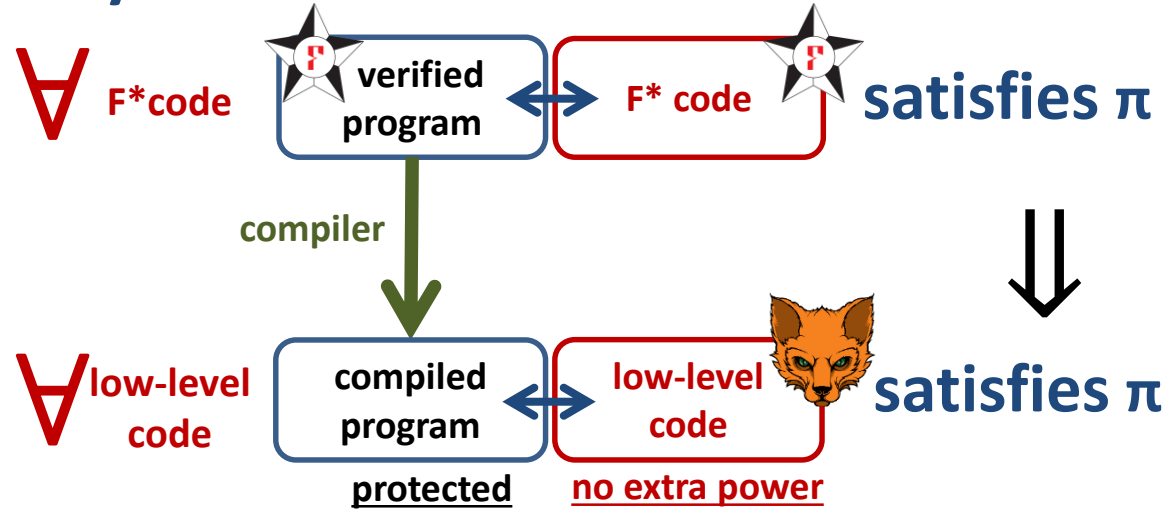
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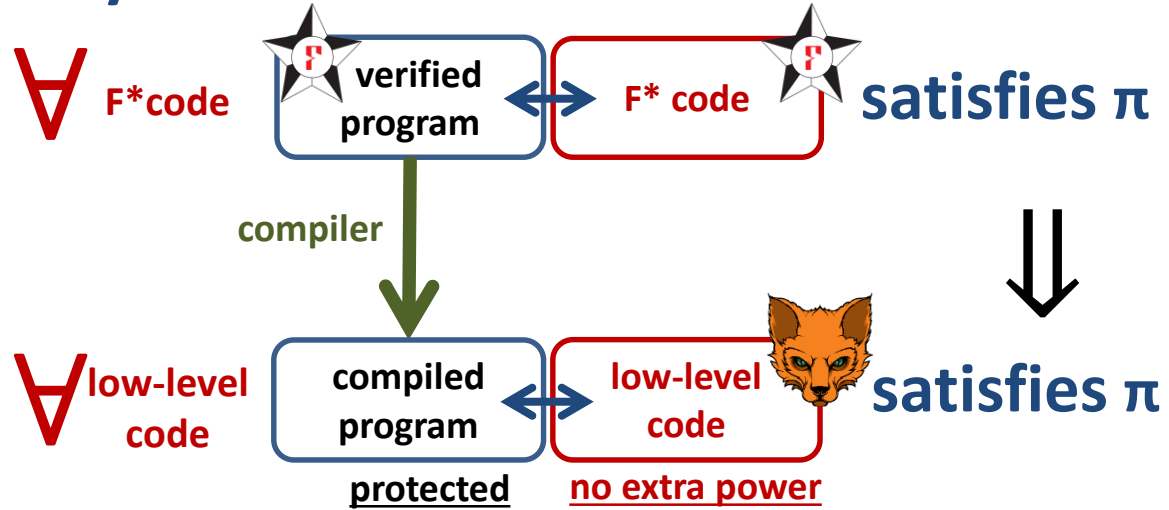
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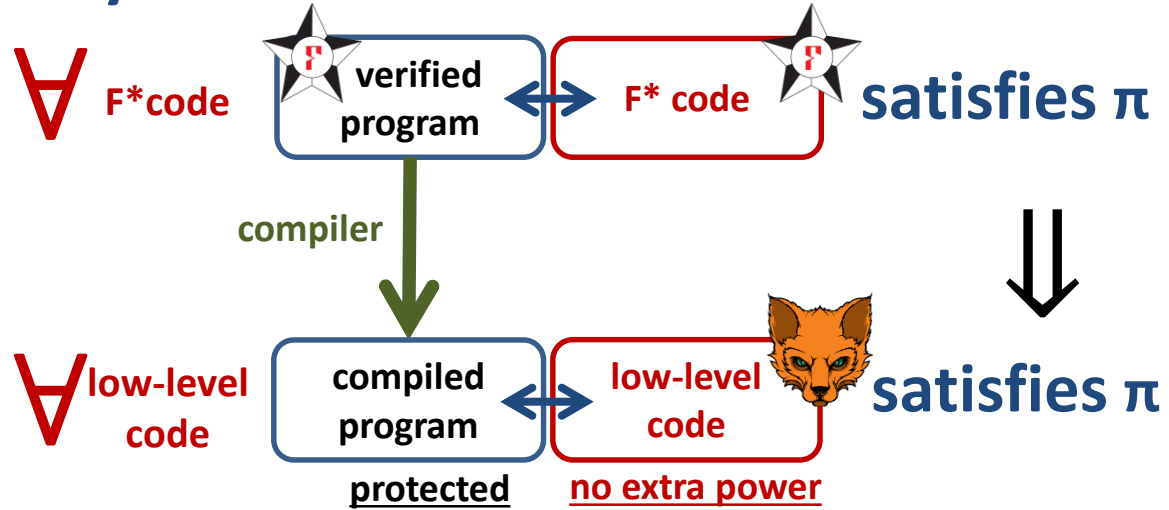


Where π can e.g. be "the web server's private key is not leaked"

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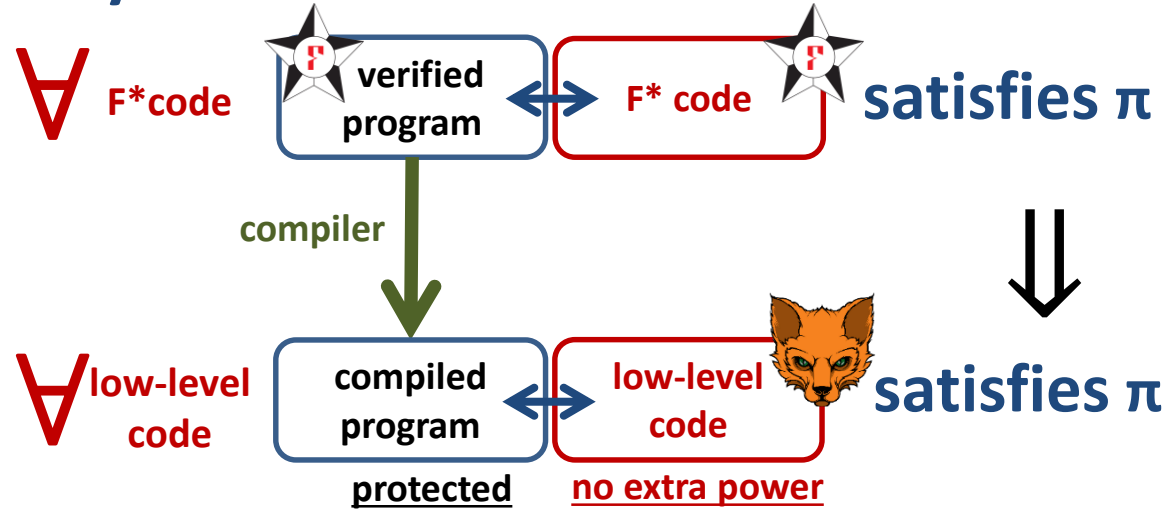


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Where π can e.g. be "the web server's private key is not leaked"

We explored many classes of properties one can preserve this way ...

Journey Beyond Full Abstraction [CSF'19, ESOP'20, TOPLAS'21]

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trace properties
(safety & liveness)

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hyperproperties
(noninterference)

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**relational
hyperproperties**
(trace equivalence)

hyperproperties
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trace properties
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Journey Beyond Full Abstraction [CSF'19, ESOP'20, TOPLAS'21]

**relational
hyperproperties
(trace equivalence)**

Robust Relational Hyperproperty
Preservation (**RrHP**)

Robust K-Relational Hyperproperty
Preservation (**RKrHP**)

Robust 2-Relational Hyperproperty
Preservation (**R2rHP**)

Robust Relational
Property Preservation (**RrTP**)

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Robust Relational
XSafety Preservation (**RrSP**)

Robust Finite-Relational
XSafety Preservation (**RFrSC**)

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Robust 2-Relational
XSafety Preservation (**R2rSP**)

+ *determinacy*

*Robust Trace Equivalence
Preservation (RTEP)*

**hyperproperties
(noninterference)**

Robust Hyperproperty
Preservation (**RHP**)

Robust Subset-Closed Hyperproperty
Preservation (**RSCHC**)

Robust K-Subset-Closed Hyperproperty
Preservation (**RKSCHP**)

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Robust Hypersafety
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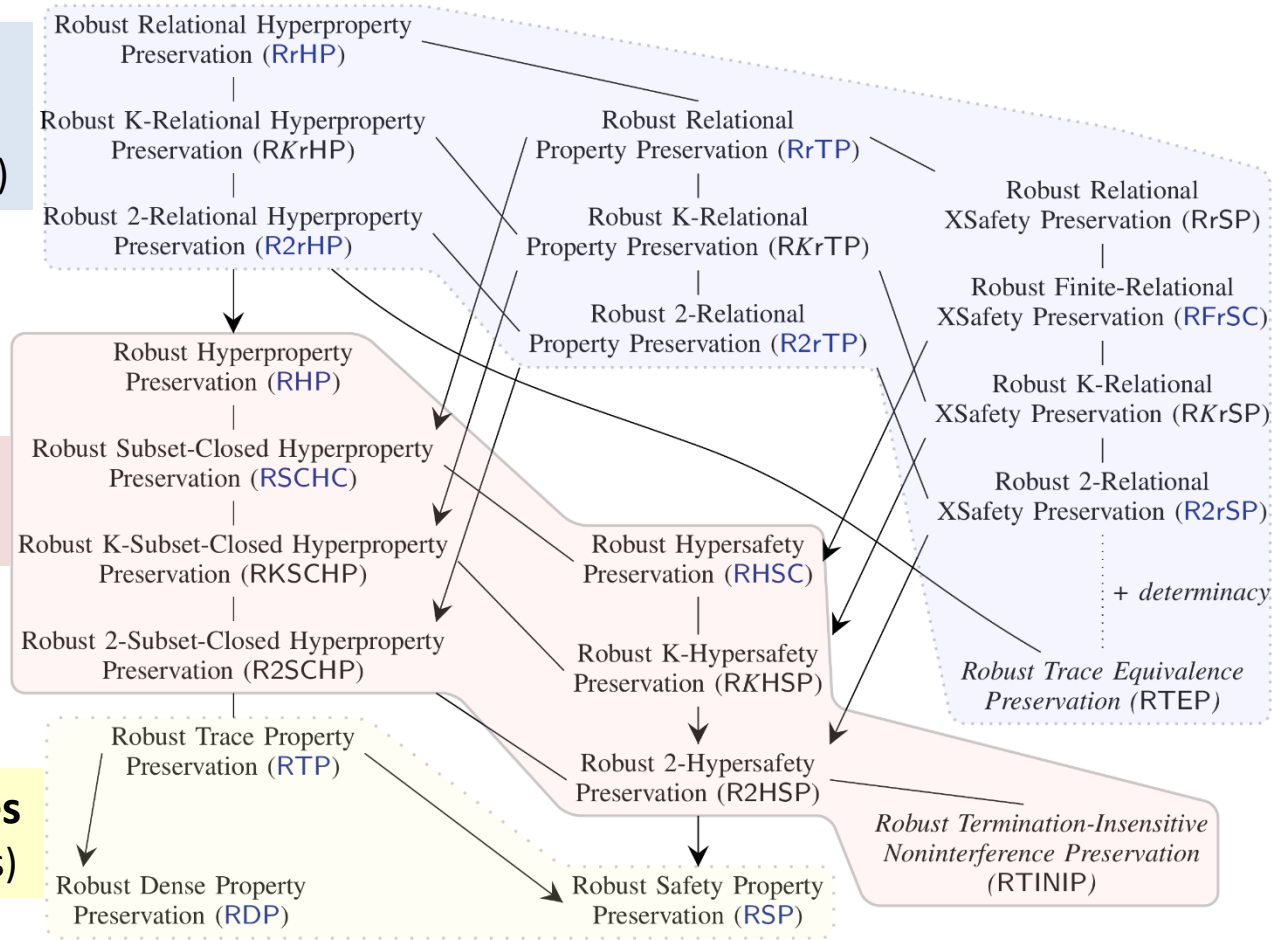
*Robust Termination-Insensitive
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(RTINIP)*

**trace properties
(safety & liveness)**

Robust Trace Property
Preservation (**RTP**)

Robust Dense Property
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Robust Safety Property
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relational hyperproperties
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No one-size-fits-all security criterion

More secure



More efficient to enforce
Easier to prove

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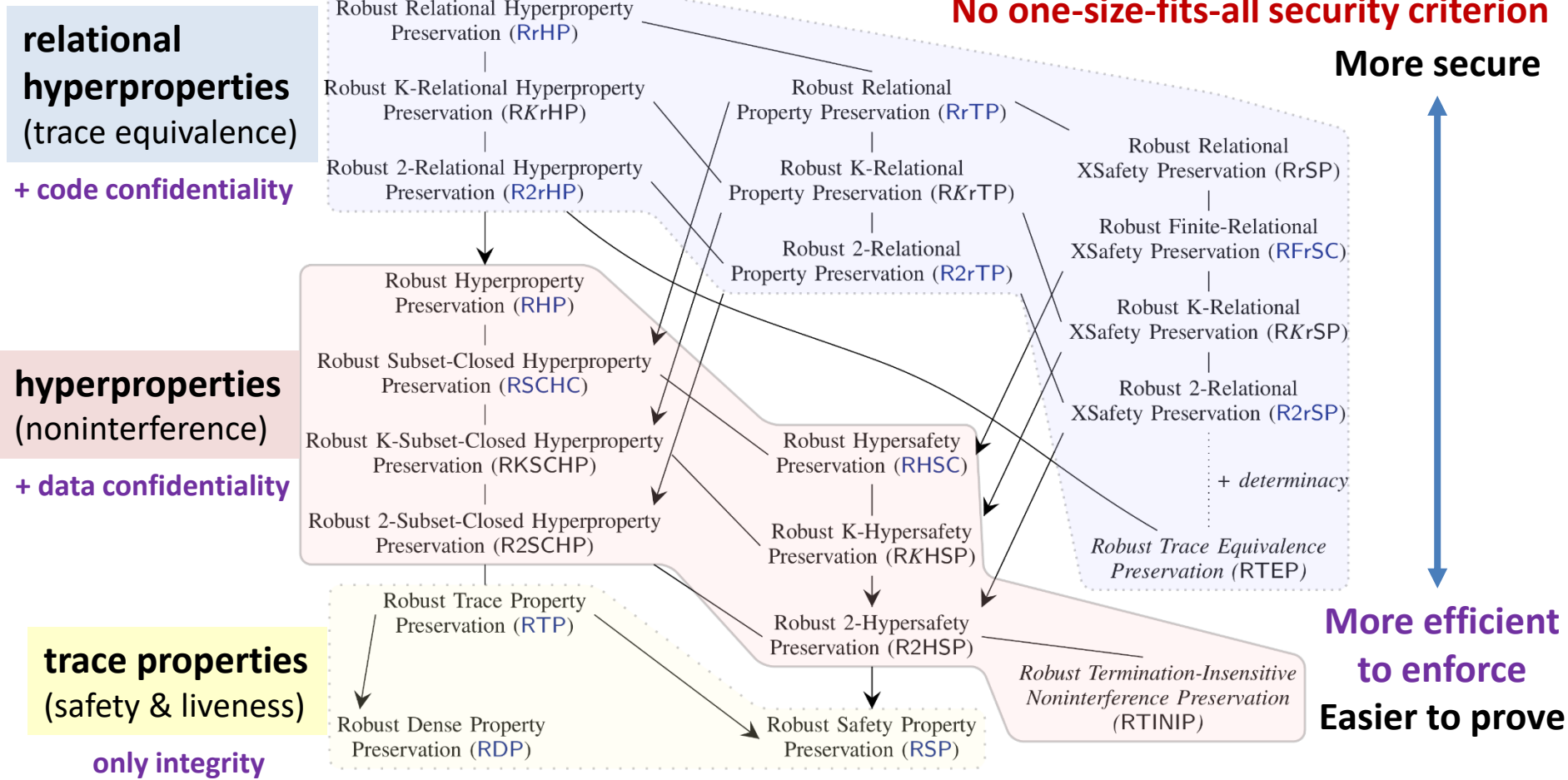
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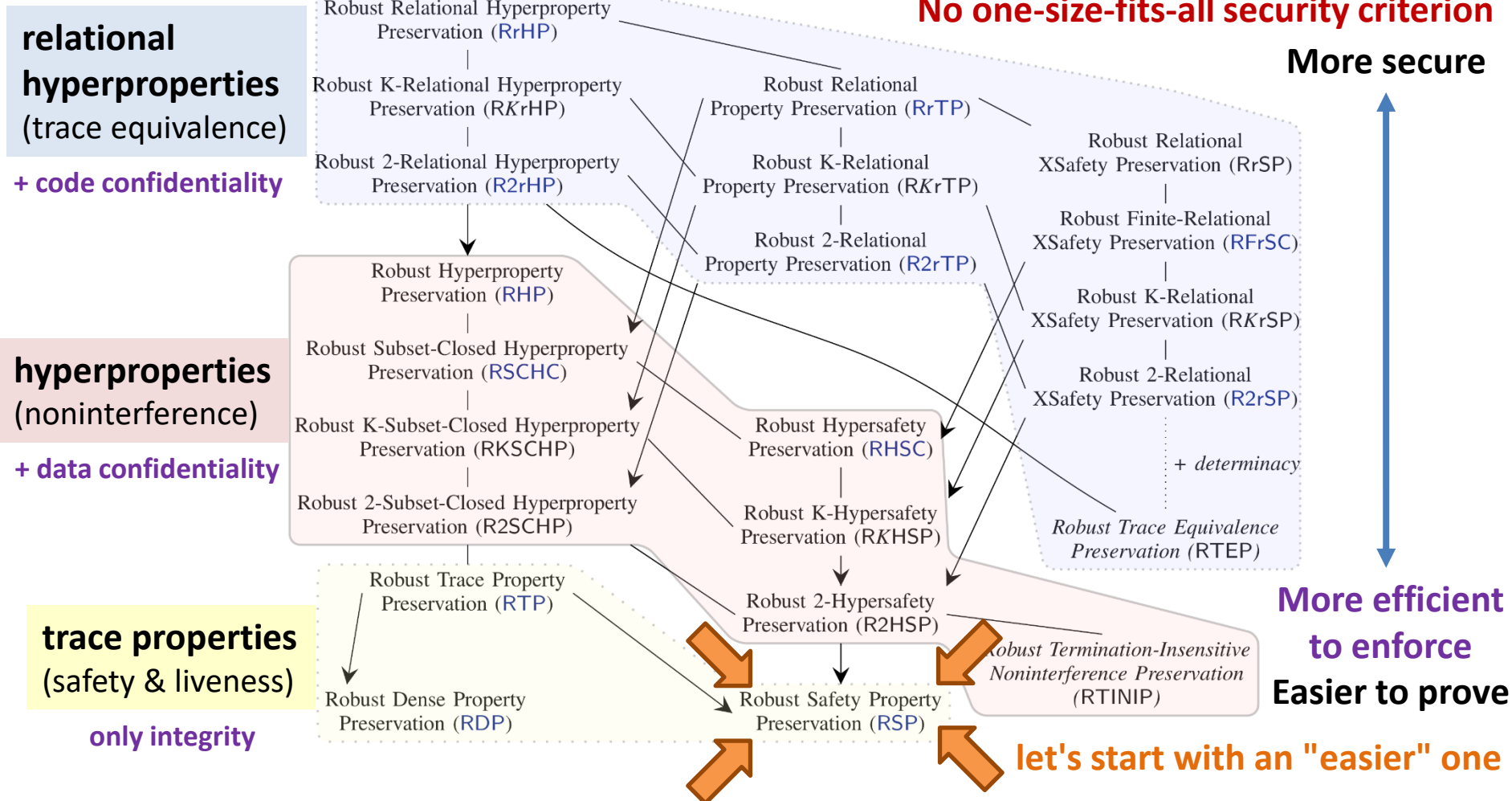


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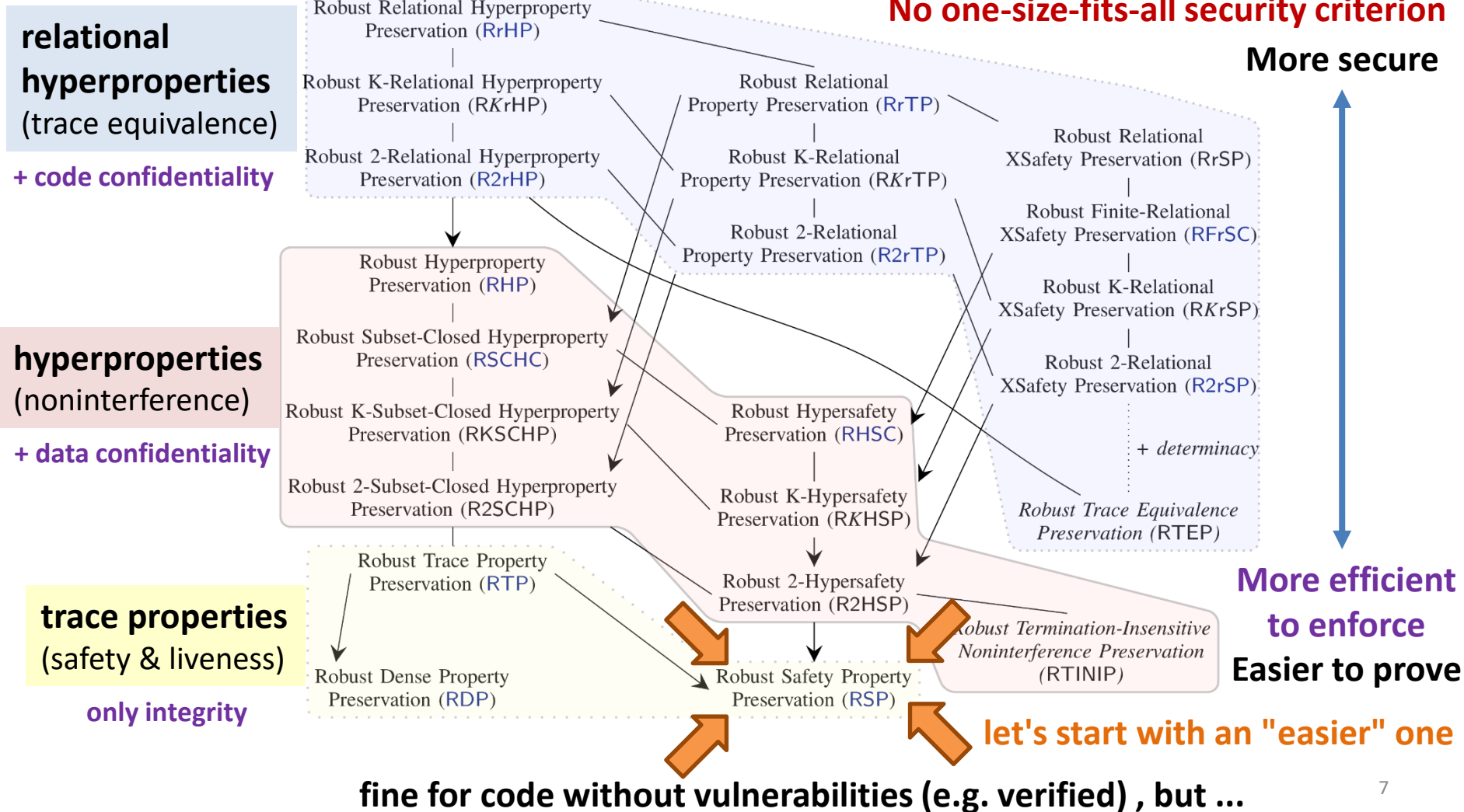
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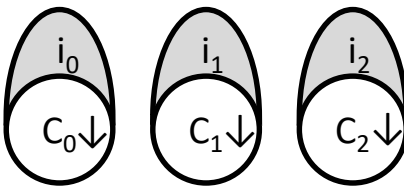


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- **We don't know when a compartment will be compromised**
 - every compartment should receive protection until compromised

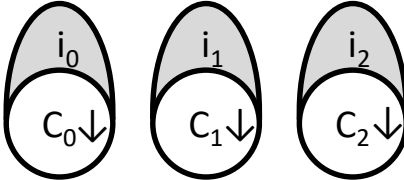


Security definition:

If  \rightsquigarrow machine m then

Security

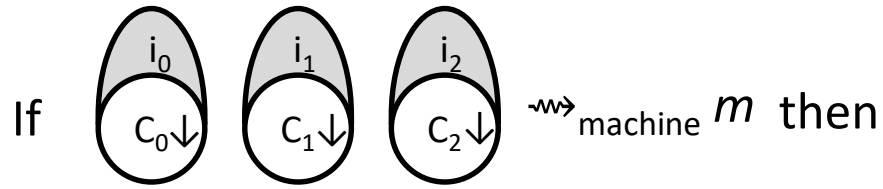
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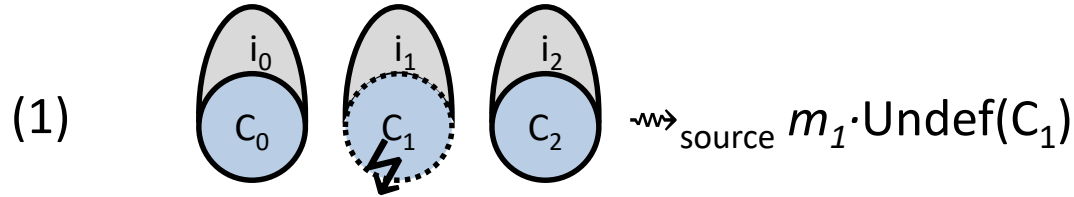
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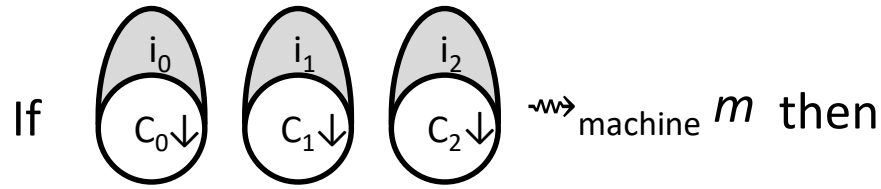


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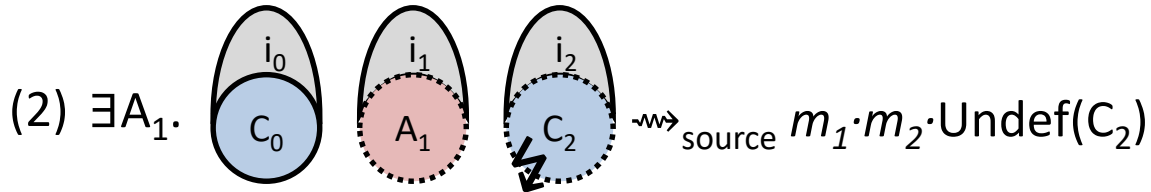
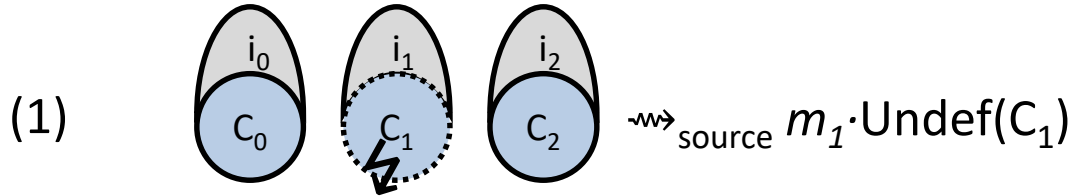


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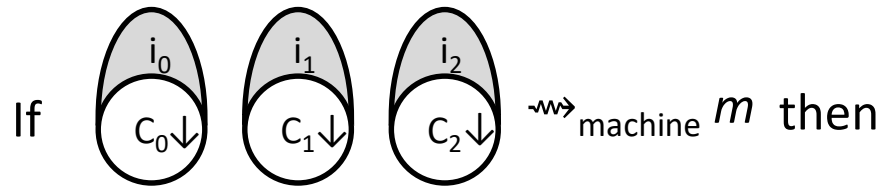


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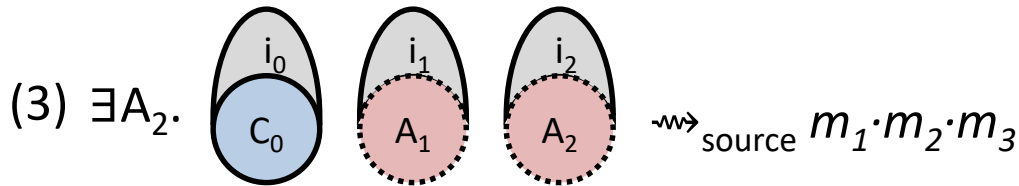
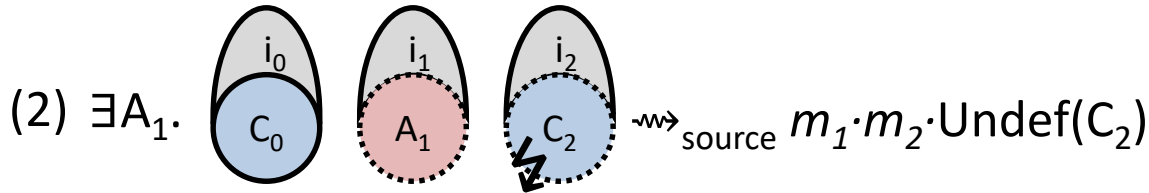
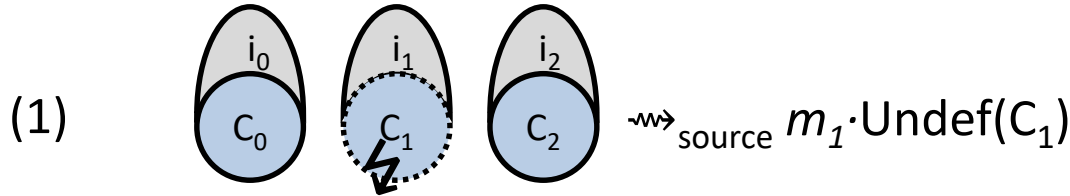


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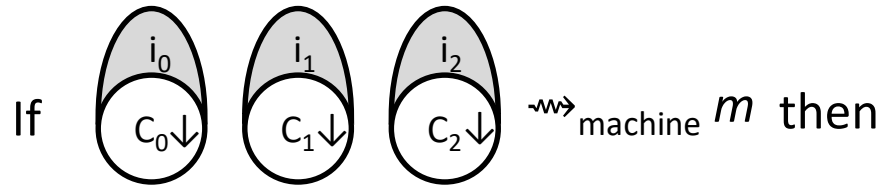


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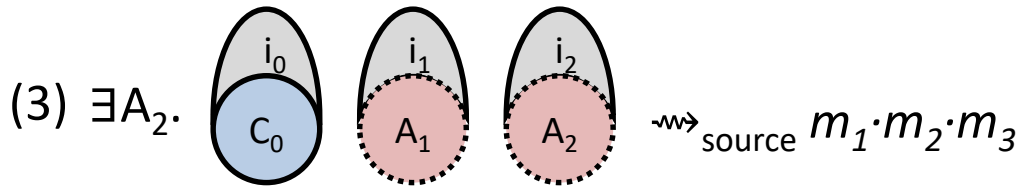
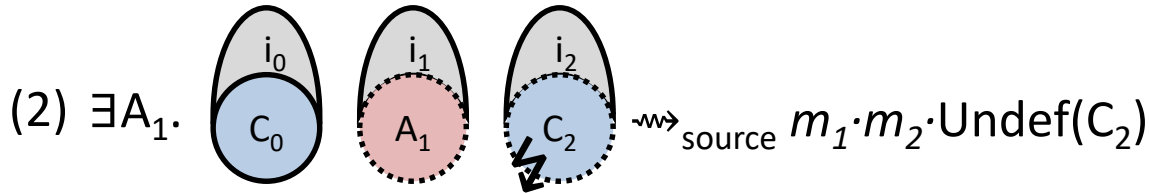
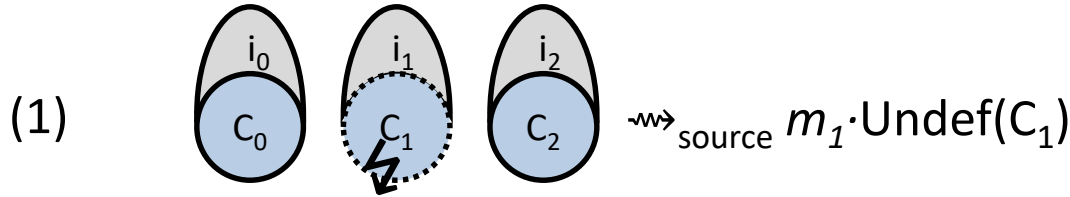


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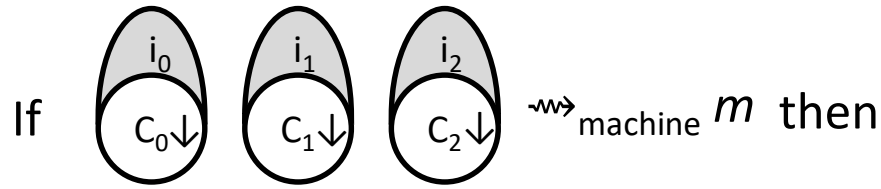
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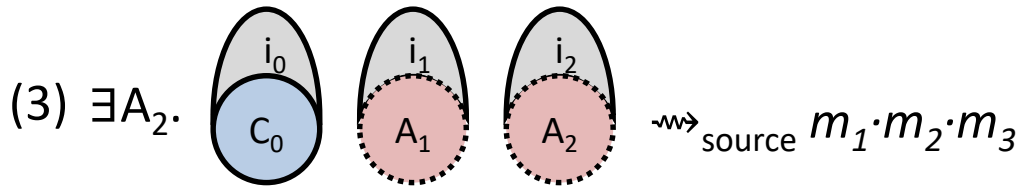
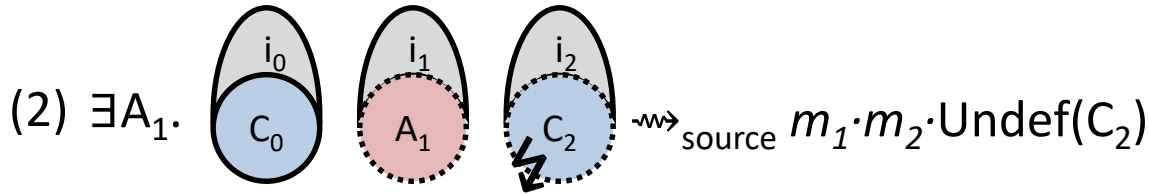
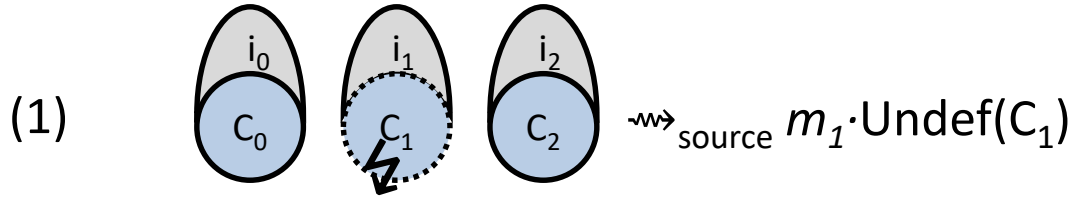
Finite trace m records which compartment encountered undefined behavior and allows us to rewind execution

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We can reduce this to a **variant of robust safety preservation** [CCS'18]

We reduce our security goal to a variant of:

Robust **Safety** Preservation

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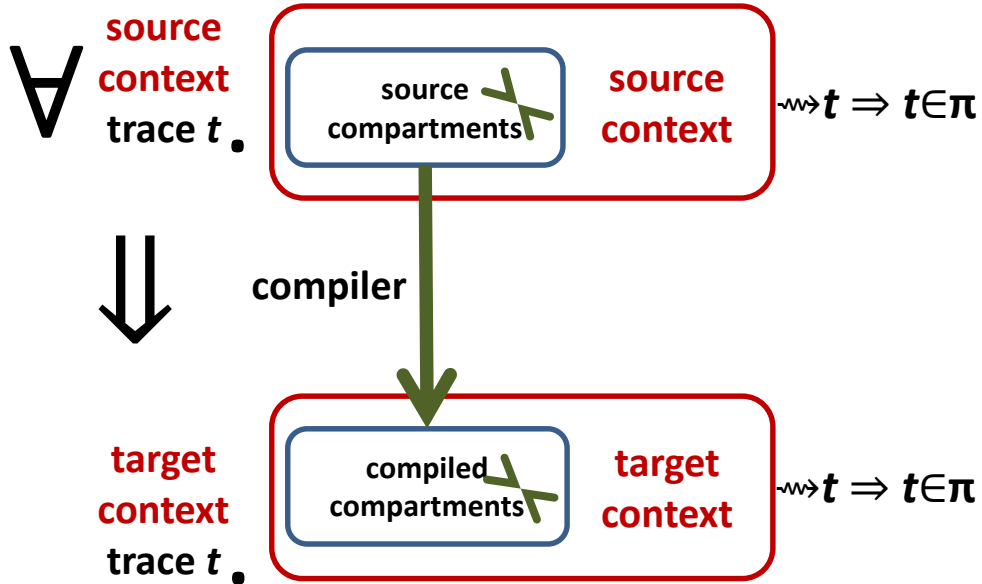


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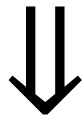


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compiler



robust preservation of safety

proof-oriented characterization

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Robust Safety Preservation

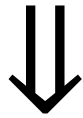
\forall source compartments.

$\forall \pi$ **safety** property.

\forall **source context** trace t .



$\rightsquigarrow t \Rightarrow t \in \pi$



compiler

\forall **target context** trace t .



$\rightsquigarrow t \Rightarrow t \in \pi$

robust preservation of **safety**

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compiler



\exists **target context**.

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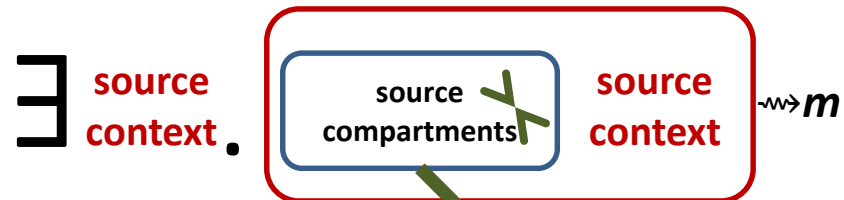
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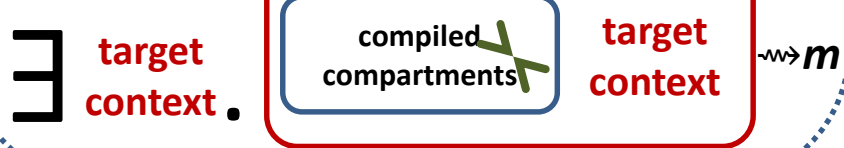
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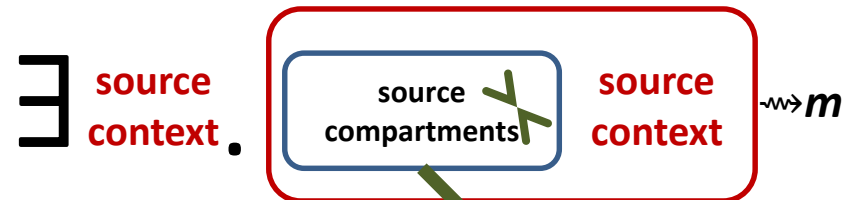
compiler



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compiler



proof-oriented characterization

2. Security Enforcement



CompCert C
with compartments 🧩

2. Security Enforcement



CompCert C
with compartments 



SECOMP: CompCert extended with secure compartments

2. Security Enforcement



CompCert C
with compartments 



SECOMP: CompCert extended with secure compartments

CompCert RISC-V ASM
with compartments 

magically secure semantics

2. Security Enforcement



CompCert C
with compartments 

SECOMP: CompCert extended with secure compartments

CompCert RISC-V ASM
with compartments 

magically secure semantics

Software-Fault Isolation

vanilla ASM

2. Security Enforcement



CompCert C
with compartments 


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Hardware-accelerated enforcement

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CHERI RISC-V 
capability machine

(inspiration for ARM Morello)

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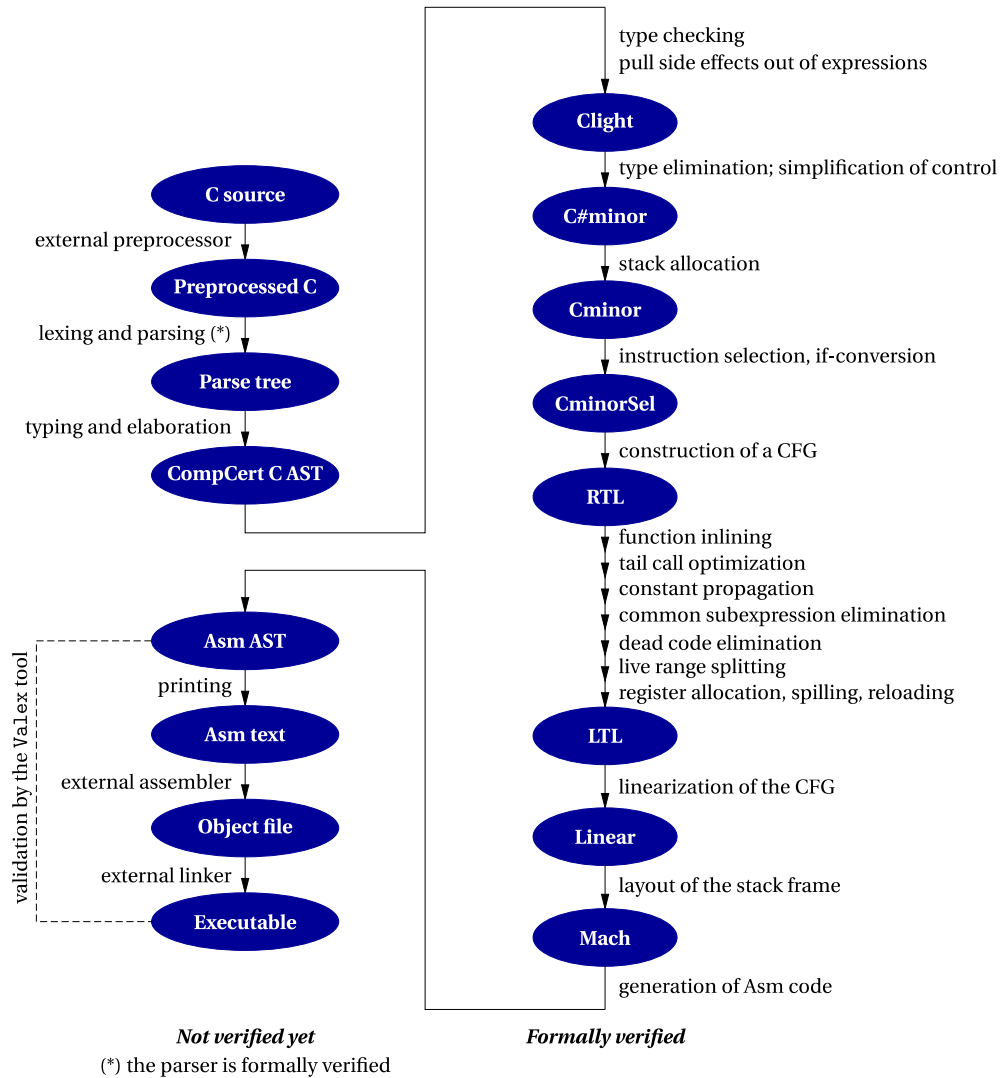
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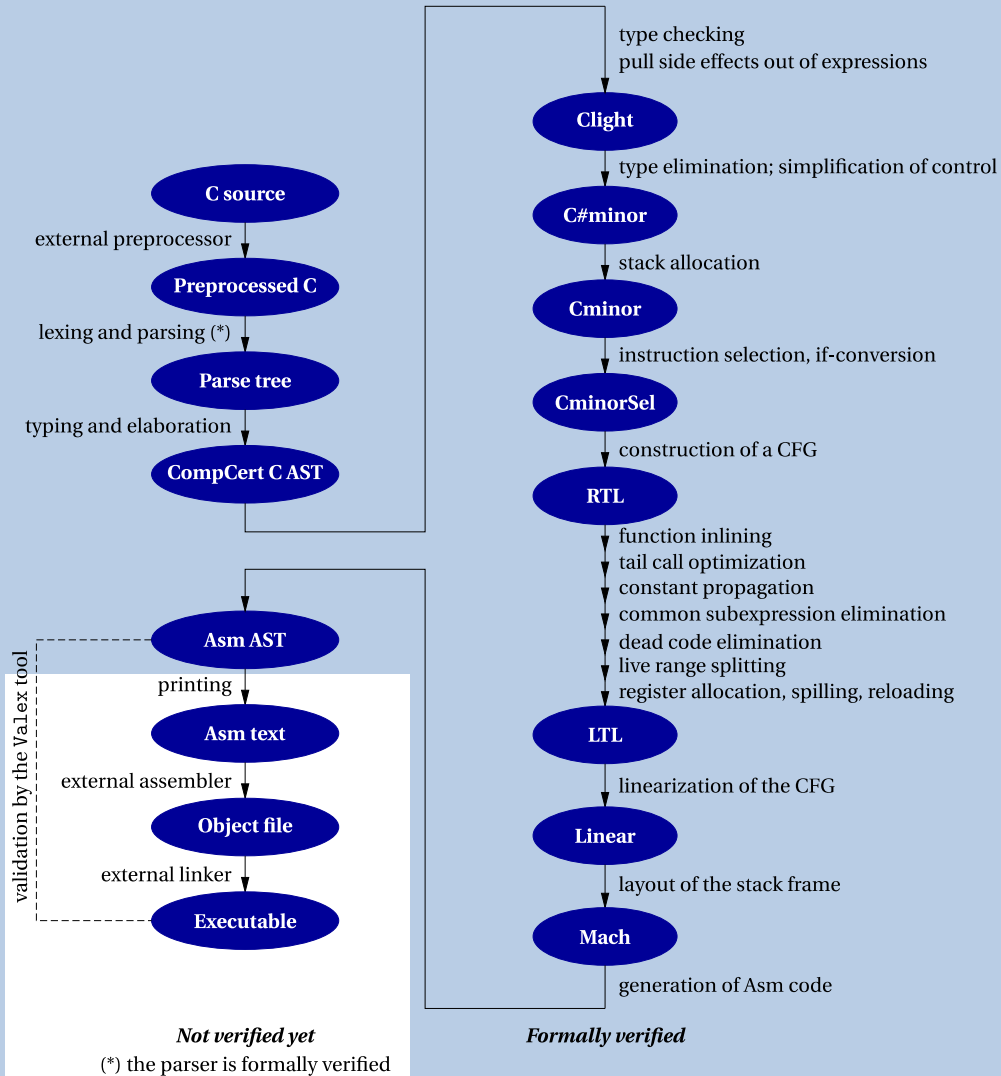
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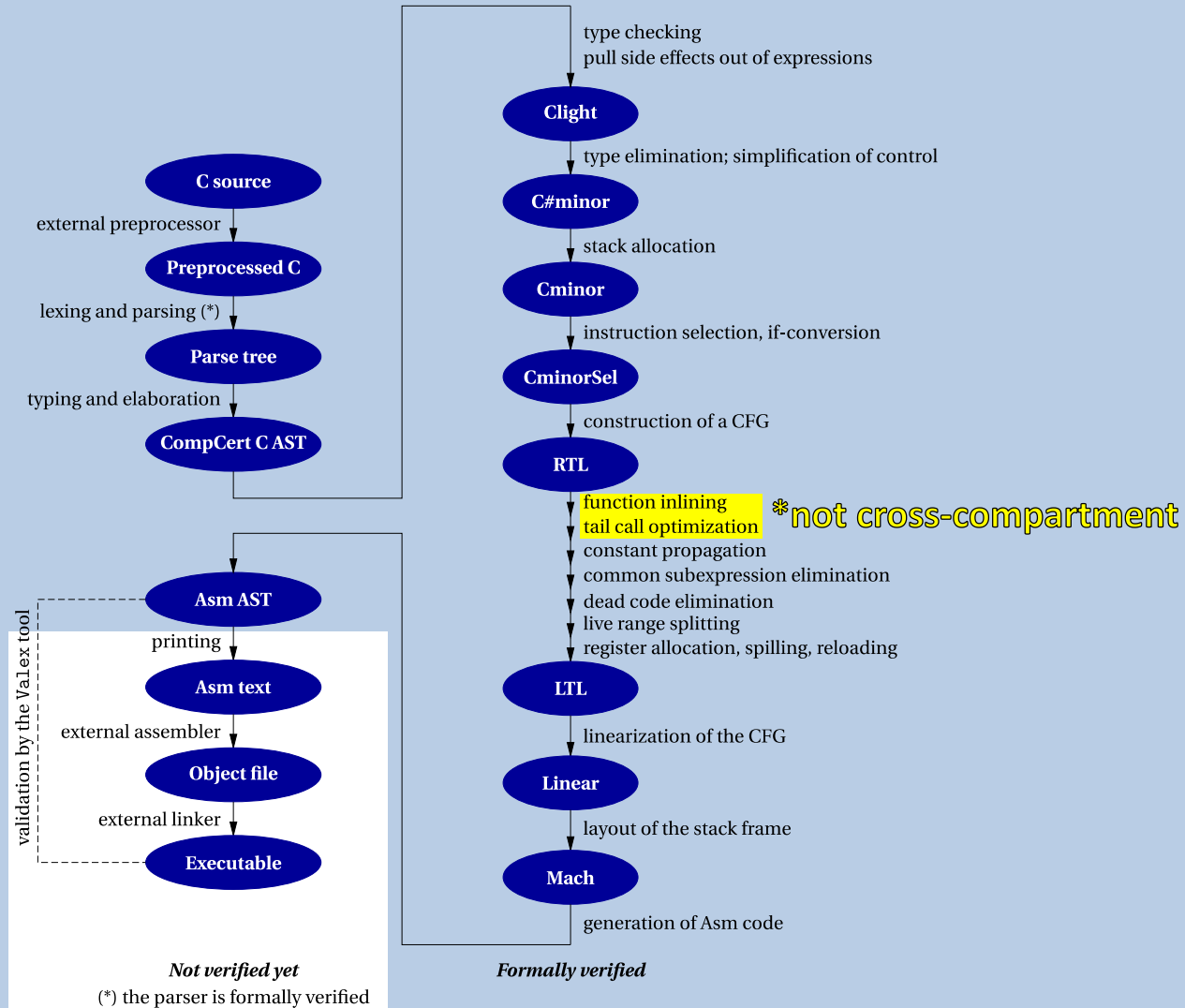
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all 19 verified compilation passes*
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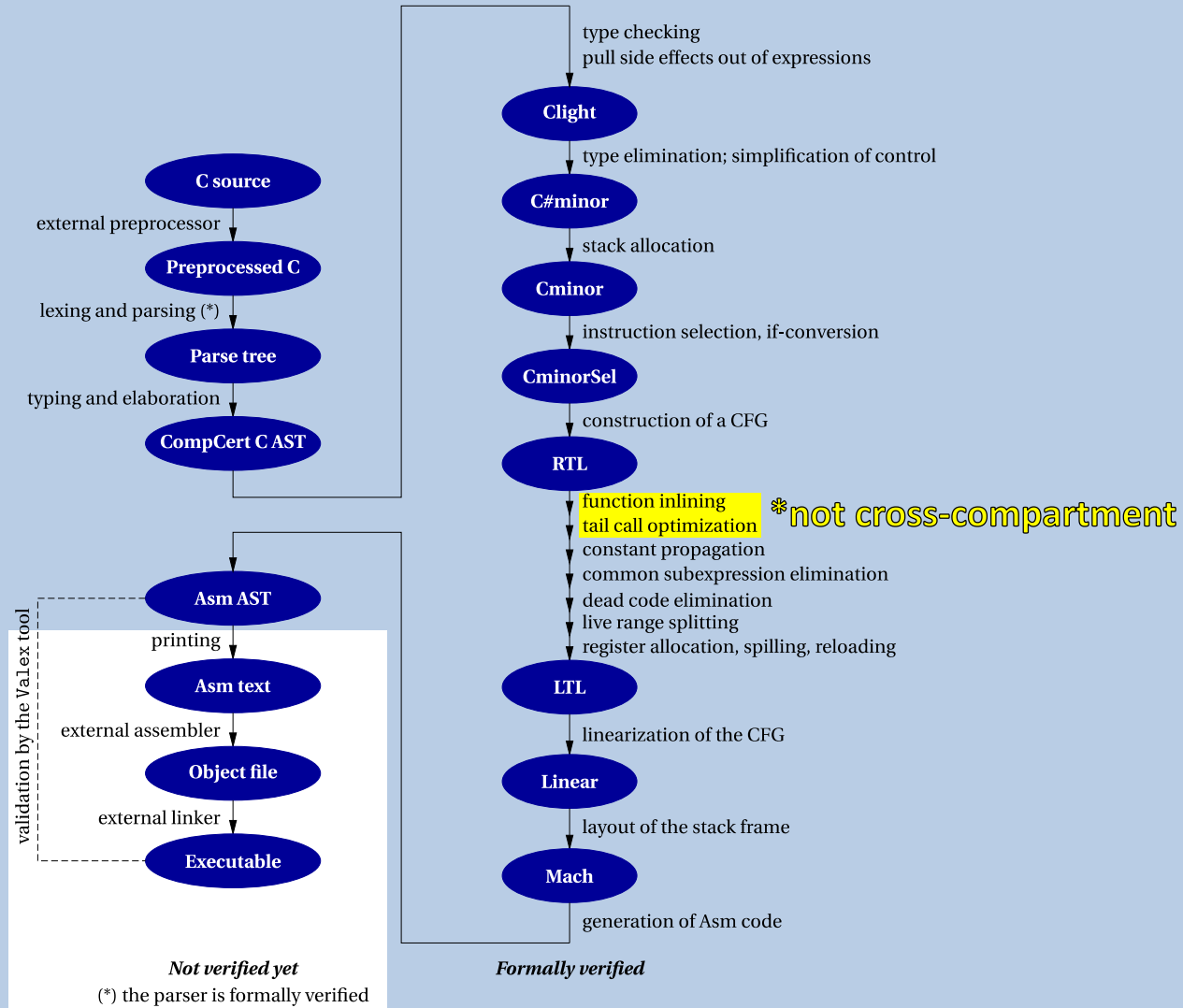
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extended compiler correctness
12+ KLoC, only 9.4% change
reused for security



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- **Need to protect stack-spilled call arguments**
 - so that malicious caller cannot exploit callbacks to covertly change arguments of a previous call
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- **Abstract machine with magically secure semantics**
 - independent of actual enforcement (lower-level backends)

Capabilities Backend

- Targeting the **CHERI RISC-V** capability machine



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- Mutual distrustful compartments: **capability-protected wrappers**
 - on calls and returns clear registers and prevent passing capabilities between compartments
- Also investigating **calling convention based solely on wrappers**
 - no new kind of capability over what CHERI already provides
 - but more interesting stack layout (not a single contiguous block)

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**Proving that our compilation chain
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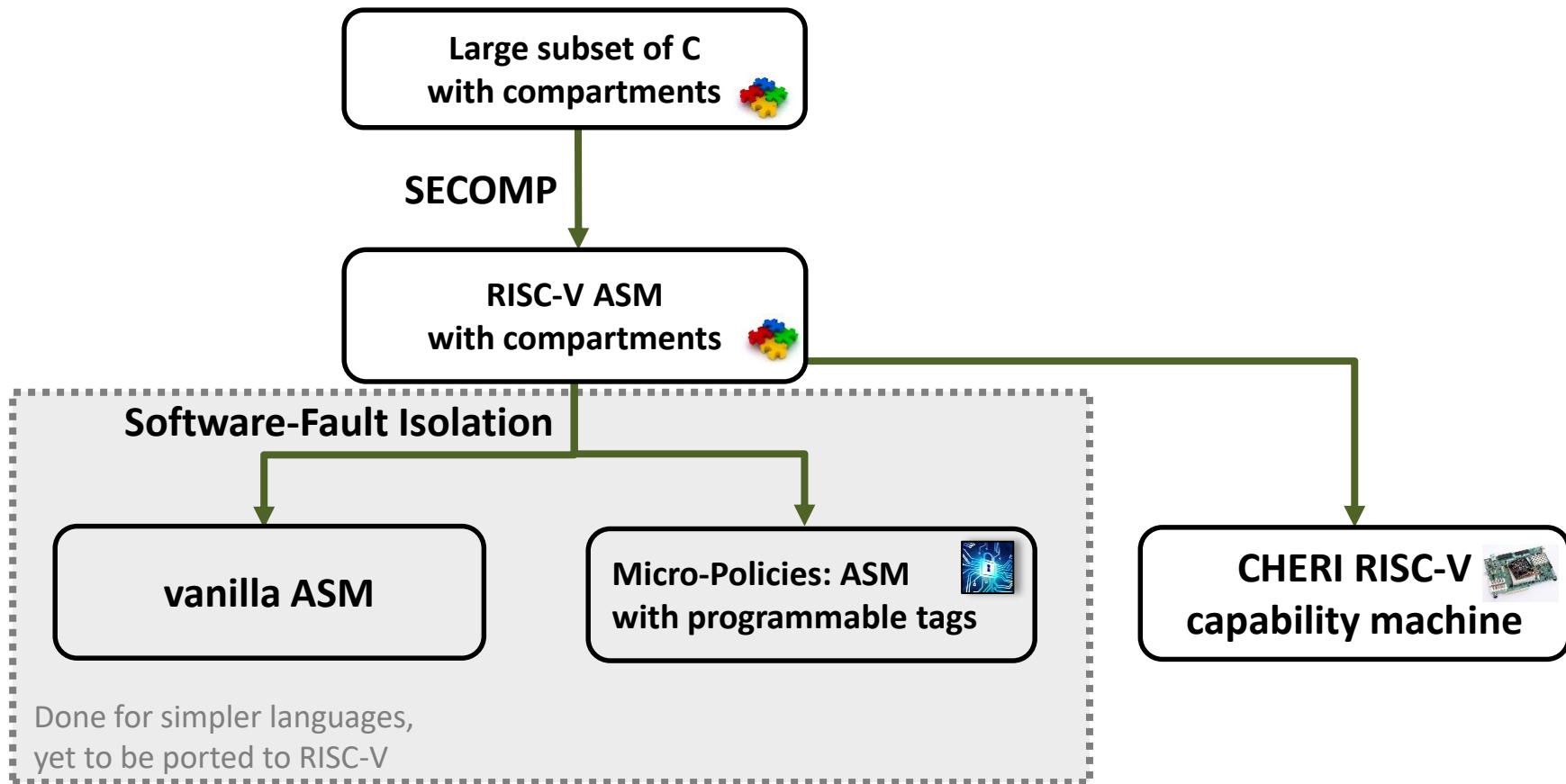


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- we focus on **machine-checked proofs** in the Coq proof assistant
 - with **property-based testing** stopgap [POPL'17, ICFP'13, ITP'15, JFP'16]
 - to find wrong conjectures early
 - to deal with the parts we couldn't (yet) verify




Secure Compilation Proofs in Coq



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Machine-checked
proofs in Coq



Large subset of C
with compartments 

SECOMP

RISC-V ASM
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Big verification challenge for the future

Systematic testing

More scalable proof technique

for our variant of Robust **Safety** Preservation [CCS'18,CSF'22]



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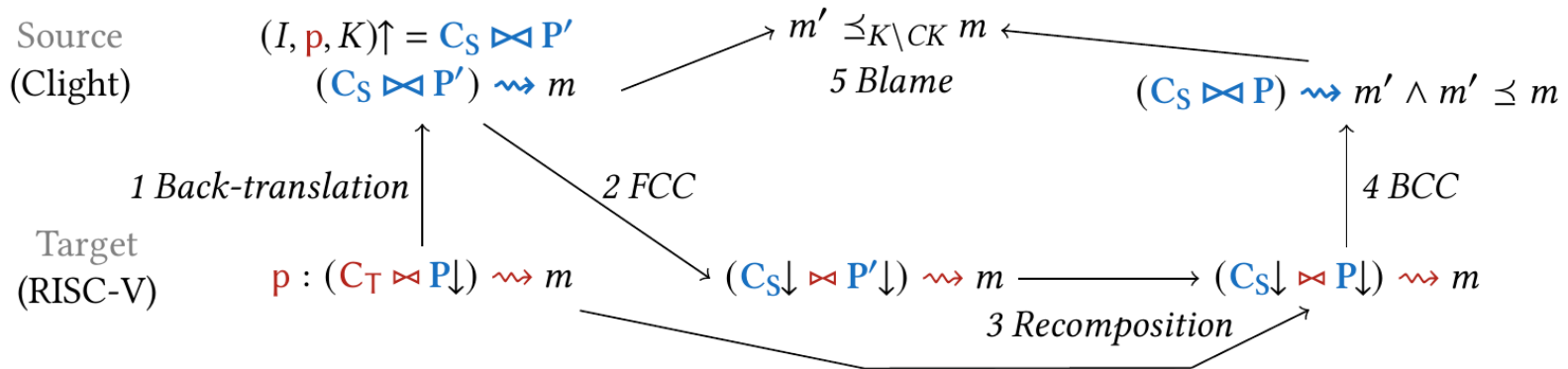
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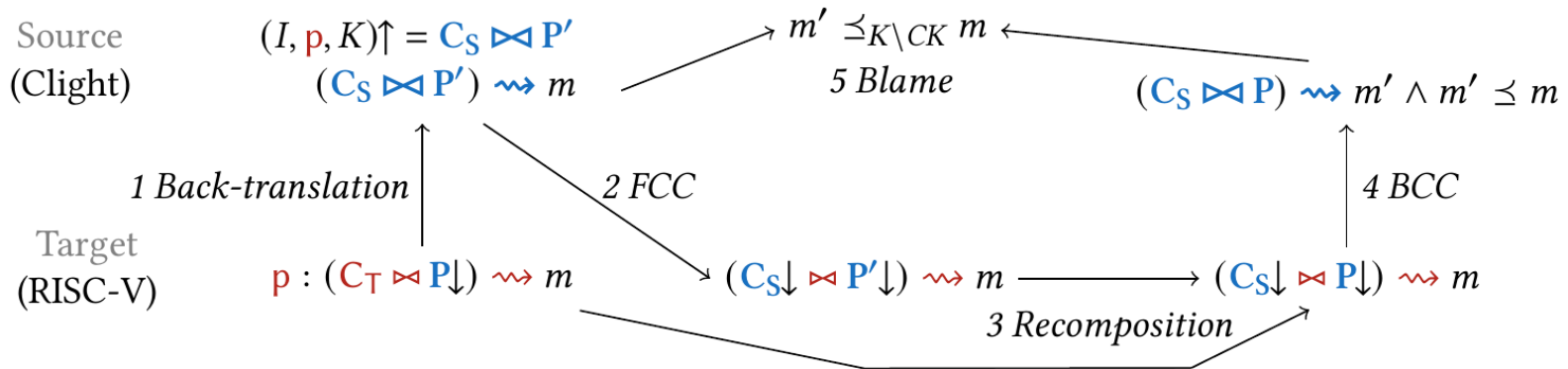


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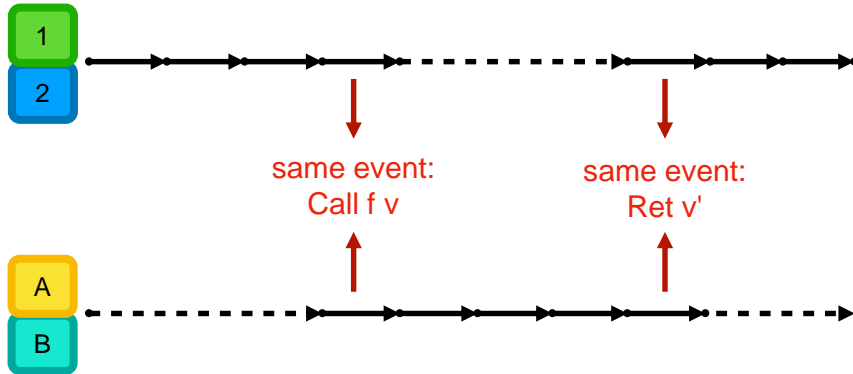


Challenging proof engineering for scaling this to CompCert [CCS'24]

Recomposition for SECOMP RISC-V

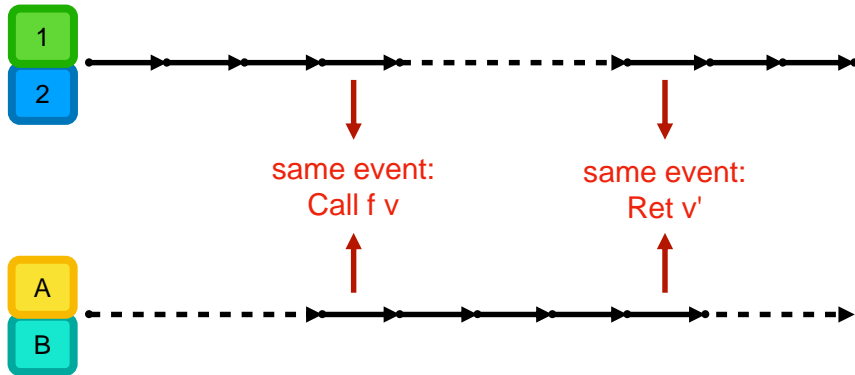
Recomposition for SECOMP RISC-V

From two synchronized RISC-V executions

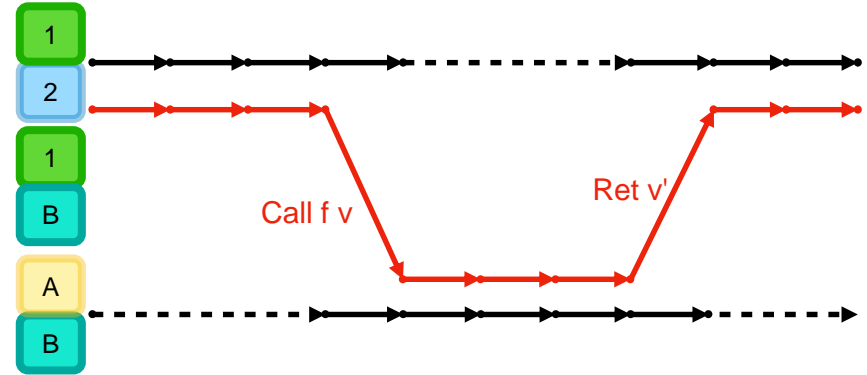


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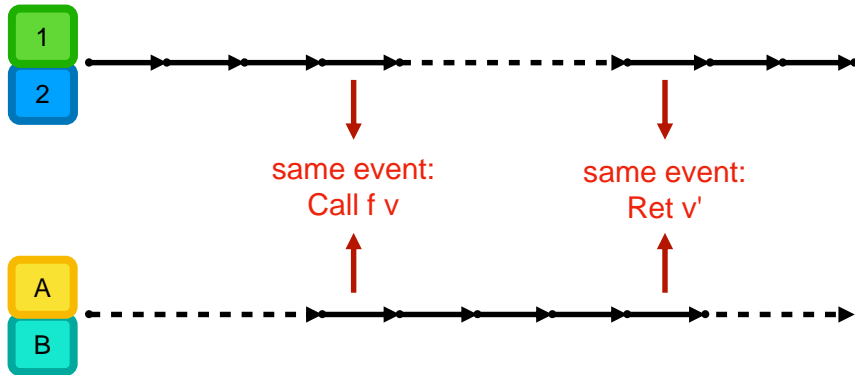


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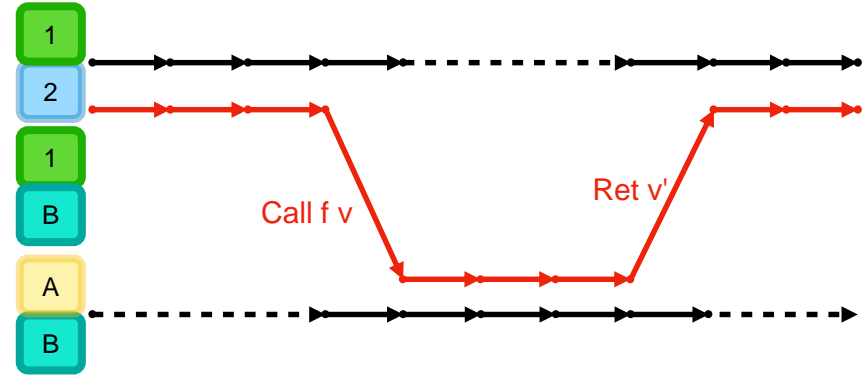


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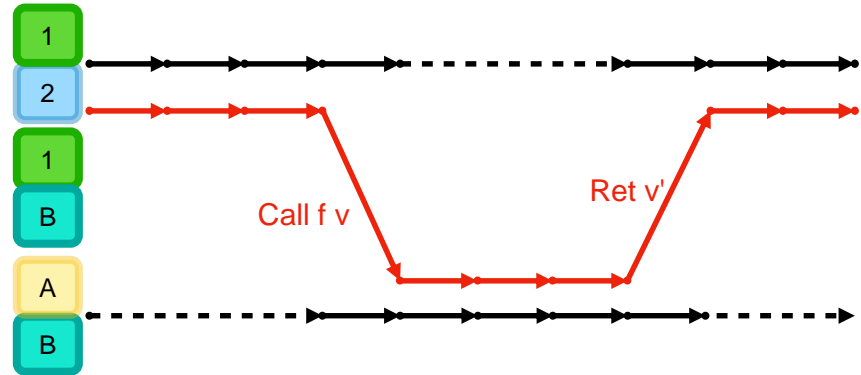
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Challenging 3-way simulation proof with subtle invariants

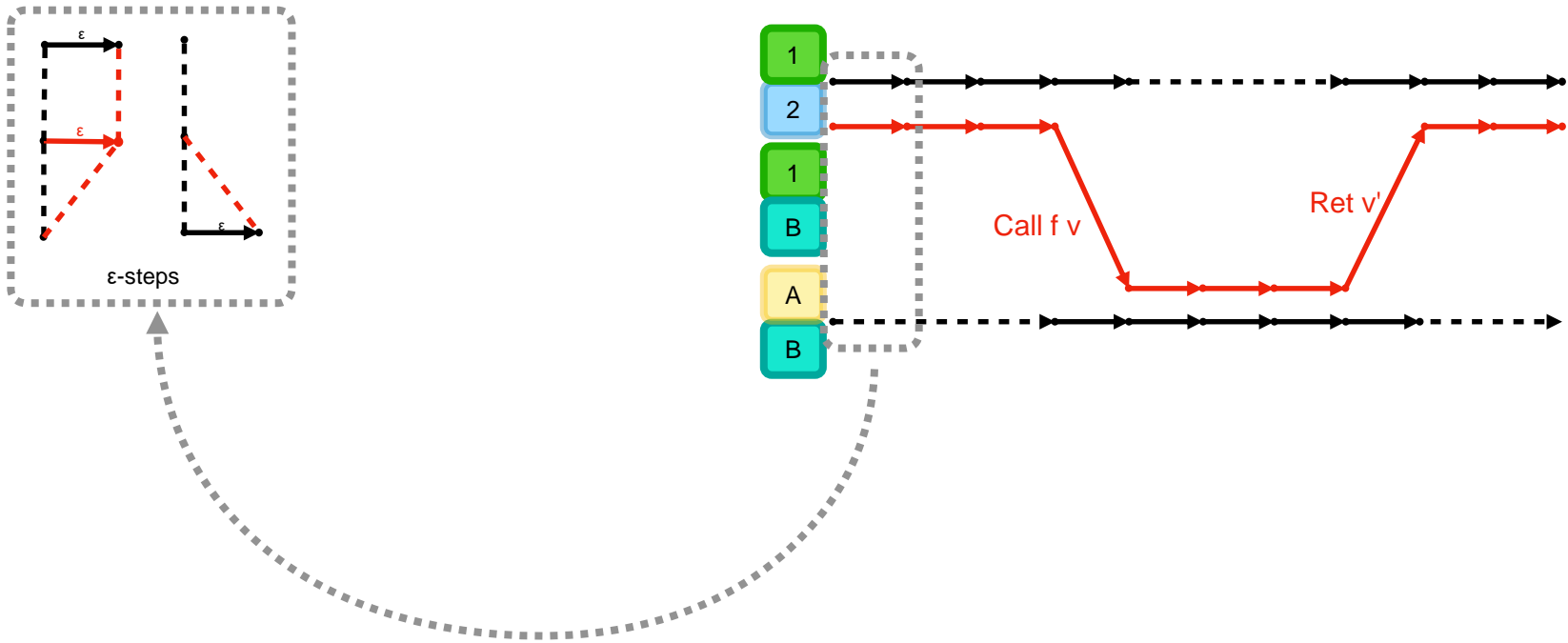
Generic diagrams for recomposition

8 generic 3-way simulation diagrams for proving recomposition



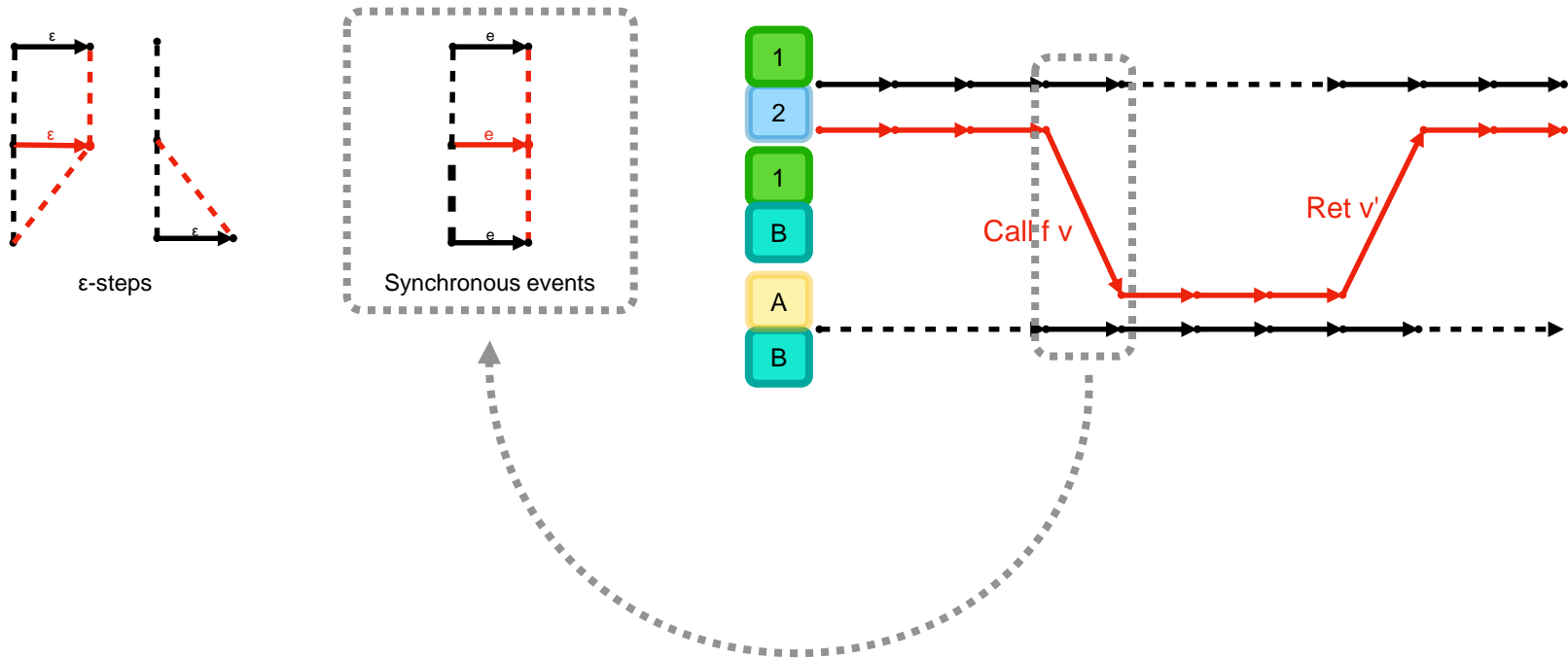
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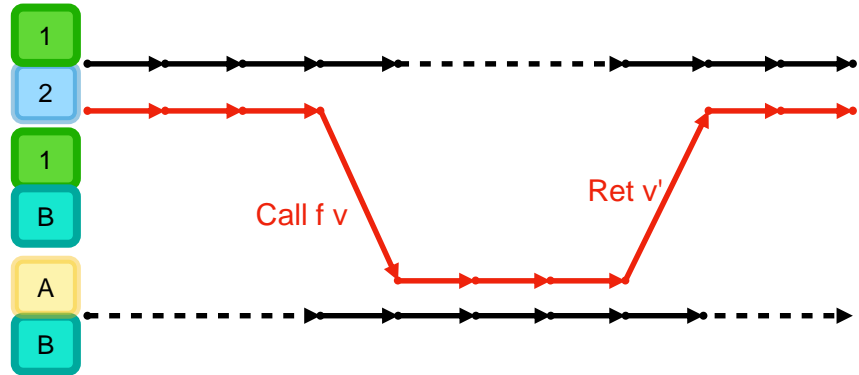
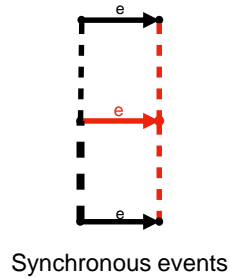
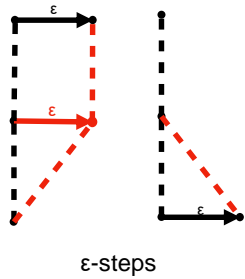
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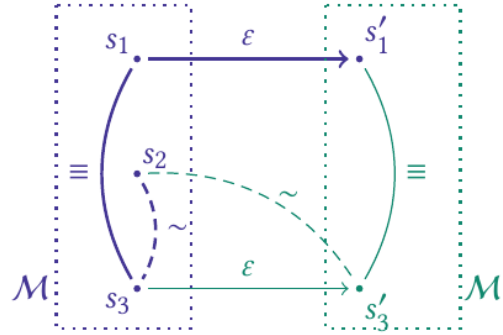


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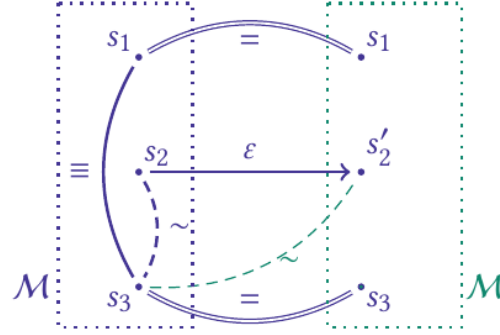
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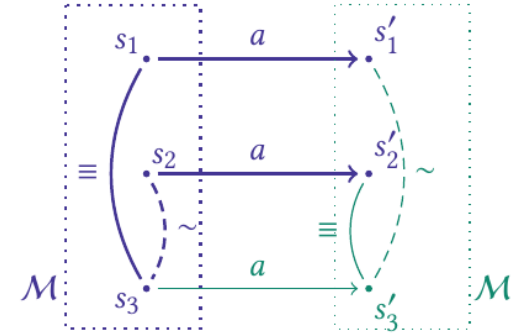
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(a) Silent step in strongly related states



(b) Silent step in weakly related states

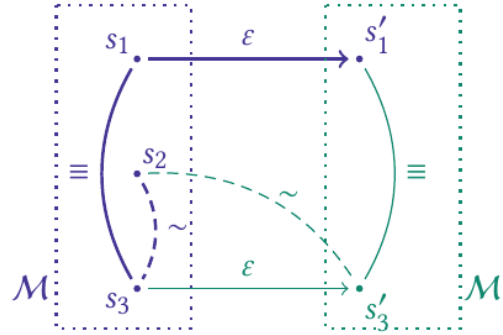


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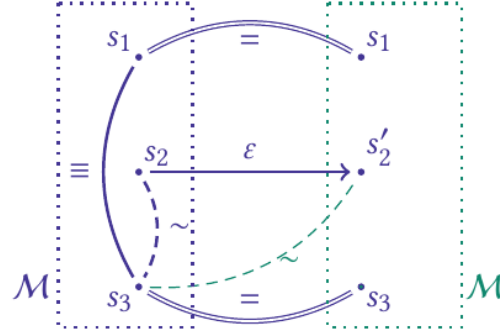
Figure 4: Recomposition diagrams

+ 5 more such diagrams

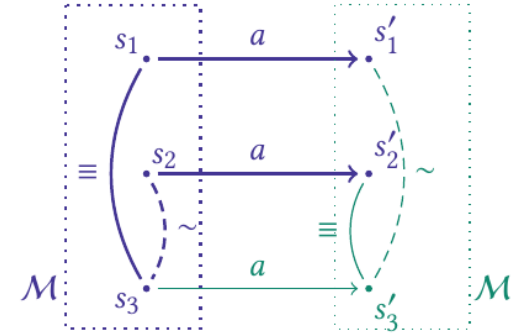
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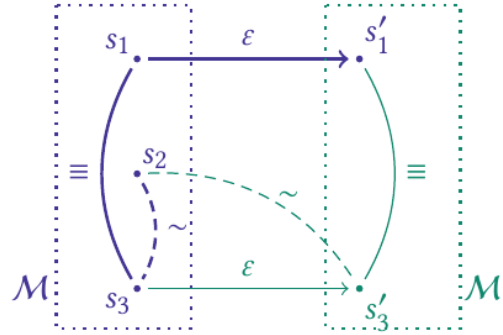
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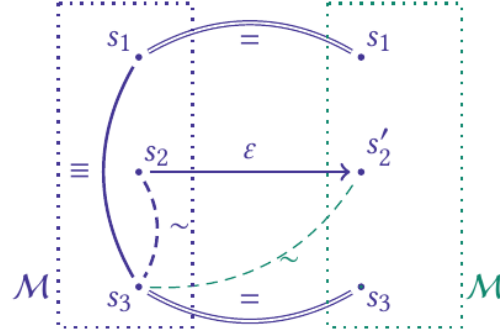
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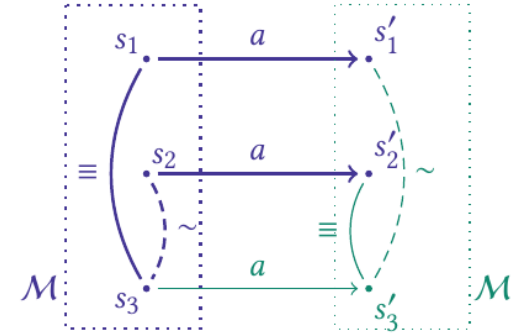
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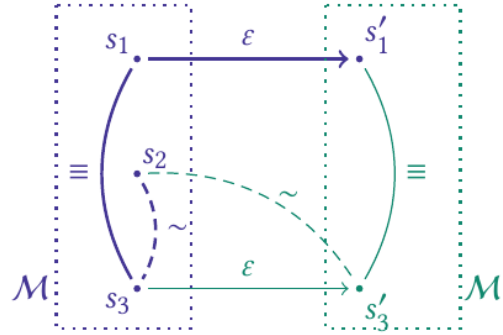
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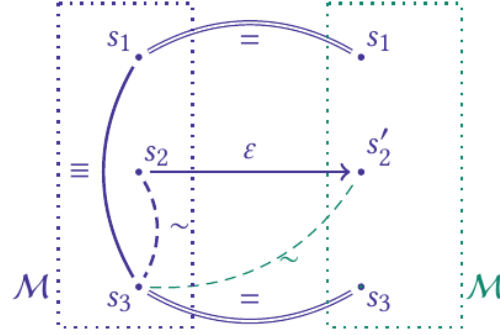
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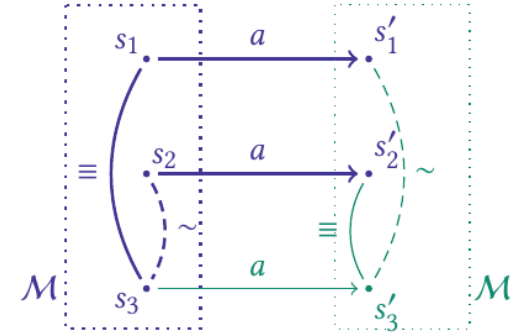
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first compiler for realistic language proved to offer strong security guarantees for compartmentalized code

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- would be nice to also have backends targeting vanilla CHERI RISC-V or Arm Morello
- would be nice to also implement a Wasm backend (software fault isolation)



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- would be nice to also have backends targeting vanilla CHERI RISC-V or Arm Morello
- would be nice to also implement a Wasm backend (software fault isolation)

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- **Verifying backends is challenging though**

- more concrete view of memory as array of bytes (vs CompCert one)
- once code stored in memory, can no longer hide all the information about compartment's code (code layout leaks)
 - proof step inspired by full abstraction doesn't work all the way down (recomposition)



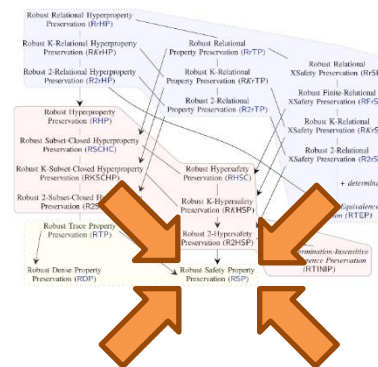
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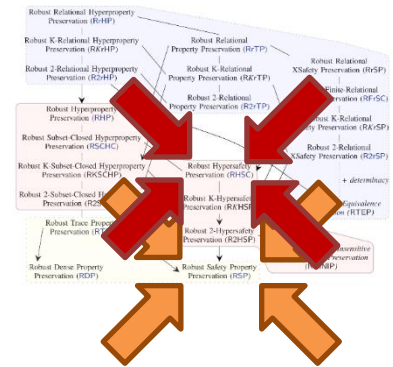
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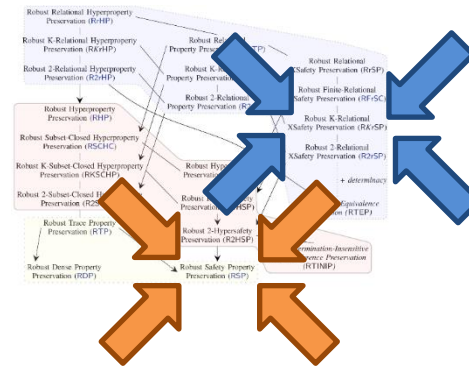
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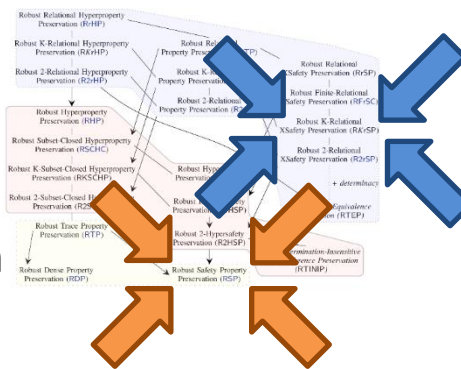
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 - even **relational hyperproperties** (observational equivalence)
 - secure compilation criteria strictly stronger than full abstraction
 - can do this for CompCert, but won't hold for backends
- [Jérémy Thibault et al, CSF'19 + more ongoing work]



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- **Combining this with compartmentalization practically interesting**
 - Especially for languages like Wasm, which are used for same-process isolation



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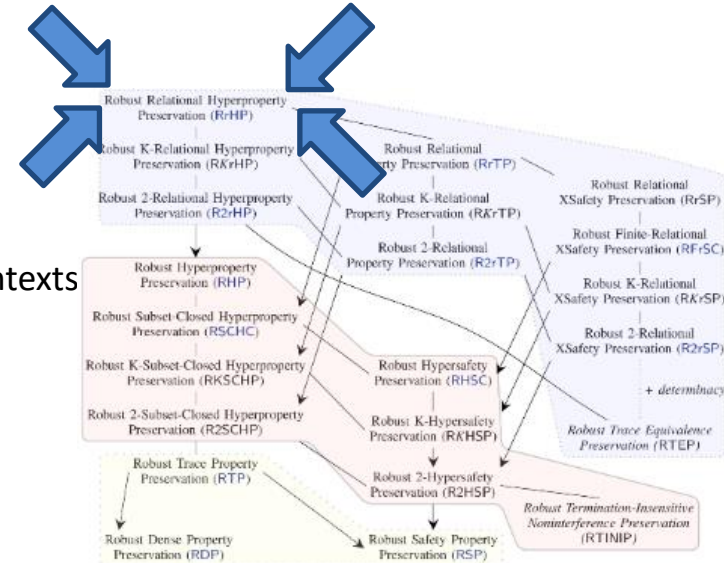
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 - preserving **all relational hyperproperties** against adversarial contexts
 - first step towards formally secure F*-OCaml interoperability



SECOMP: Formally Secure Compilation of Compartmentalized C Programs



1. Goal: formalized end-to-end security guarantees

- preserve properties **against adversarial contexts**
- we overcame additional challenges to support **mutually distrustful compartments** and **dynamic compromise**



2. Enforcement: protect abstractions all the way down

- **Extended CompCert languages with compartments**
- **Unverified backend targeting CHERI RISC-V capability machine**



3. Proof: verify security of our compilation chain

- **more scalable proof technique machine-checked in Coq**
- first compiler for realistic language proved to offer strong security guarantees for compartmentalized code

