Formally Secure Compilation

Cătălin Hrițcu

Inria Paris

https://secure-compilation.github.io

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Part 1: formalize what it means to solve this problem

Part 2: give meaning to mitigation (protected components) inherently insecure languages like C/C++

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Part 1: formalize what it means to solve this problem

Part 1 of 2

Secure Interoperability with Lower-Level Code





Carmine Abate

Inria Paris U. Trento



Deepak Garg **MPI-SWS**



Cătălin Hriţcu Inria Paris



Jérémy Thibault

Inria Paris **ENS Rennes**



Marco Patrignani

CISPA Stanford

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- e.g. HACL* and miTLS written in Low* which provides:
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 - higher-level abstractions associated with ML-like languages
 - most features of verification systems like Coq and Dafny
 - patterns specific to cryptographic code

Abstractions not enforced when linking with adversarial low-level code



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Insecure interoperability: compromised (or malicious) application linking in miTLS can easily **read and write miTLS's data and code**, **jump to arbitrary instructions**, **smash the stack**, ... ⁵

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 - no "low-level" attacks
 - no need to worry about the compilation chain (compiler, linker, loader, runtime, system, hardware)











But what does "secure" mean?

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- Study preserving various classes of ...
 - trace properties (safety, liveness)
 - hyperproperties (e.g. noninterference)
 - relational hyperproperties (e.g. trace equivalence)

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- No "one-size-fits-all solution"
 - e.g. full abstraction does **not** imply the other criteria we study
 - **stronger** criteria are **harder** to achieve and prove, both challenging

Robust Relational More secure Hyperproperty Preservation Robust k-Relational **Robust Relational Robust Relational** Hyperproperty Preservation **Property Preservation** Hypersafety Preservation Robust 2-Relational Robust K-Relational **Robust Relational** Hyperproperty Preservation **Property Preservation** Safety Preservation Robust 2-Relational **Robust Finite-Relational** Trace Equivalence Robust Hyperproperty **Property Preservation** Safety Preservation Preservation Preservation **Robust K-Relational** Robust Subset-Closed Safety Preservation Hyperproperty Preservation **Robust 2-Relational** Robust K-Subset-Closed **Robust Hypersafety** Safety Preservation Hyperproperty Preservation Preservation + determinacy Robust K-Hypersafety Robust 2-Subset-Closed Preservation Hyperproperty Preservation **Observational Equivalence** Robust 2-Hypersafety **Robust Trace** Preservation Preservation **Property Preservation** More efficient **Robust Liveness Robust Safety** Preservation Preservation Easier to prove

\forall source component.

$\forall \pi \text{ trace property.}$



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preservation of robust satisfaction


















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- Mapped the space of secure compilation criteria based on robust "property" preservation
 - Property-free characterizations and implications in Coq
 - Separation results (e.g. robust safety/liveness preservation strictly weaker than robust trace property preservation)
 - Surprising collapse between preserving all hyperproperties and preserving just hyperliveness
- Showed that even strongest criterion is achievable
 - for simple translation from a statically to a dynamically typed language with first-order functions and I/O

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- Scalable proof techniques for other criteria
 - (hyper)liveness preservation (possible?)
- Nontrivial relation between source and target traces









Part 2 of 2 When Good Components Go Bad

Secure Compilation Despite Dynamic Compromise

https://arxiv.org/abs/1802.00588

Collaborators for Part 2



Carmine Abate



Arthur Azevedo de Amorim



Rob Blanco







Chi ha detto che il buon cioccolato è



Cătălin Hriţcu



Yannis Juglaret

Théo Laurent



Benjamin **Pierce**



Marco Stronati



Andrew Tolmach

Inria Paris CMU U. Virginia U. Trento Paris 7 ENS Paris Portland State UPenn

```
#include <string.h>
int main (int argc, char **argv) {
    char c[12];
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Buffer overflow



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Practical mitigation: compartmentalization

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Goal 1: Formalize this

- Add components to C
 - interacting only via **strictly enforced interfaces**





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- Enforce "component C" abstractions:
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- Secure compilation chain:
 - compiler, linker, loader, runtime, system, hardware
- Use efficient enforcement mechanisms:
 - OS processes (all web browsers) WebAssembly (web browsers)
 - software fault isolation (SFI)
 - hardware enclaves (SGX)





- capability machines
- tagged architectures

Goal 1: Formalizing the security of compartmentalizing compilation

- Mutually-distrustful components
 - restrict **spatial** scope of undefined behavior

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• Dynamic compromise

- restrict temporal scope of undefined behavior

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- shouldn't percolate before earlier observable events
 - careful with code motion, backwards static analysis, ...

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- GCC and LLVM currently violate this model

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- a component only loses guarantees after an attacker discovers and exploits a vulnerability
- the mere existence of vulnerabilities doesn't immediately make a component compromised





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 \exists a **dynamic compromise scenario** explaining *t* in source language for instance leading to the following compromise sequence:

(0)
$$(c_0)$$
 (c_1) (c_2) (c_2) (c_1) (c_1) (c_2) (c_2) (c_2) (c_1) (c_1) (c_1) (c_2) (c_2)



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Mutual distrust
$$(c_1)$$
 (A_2) (c_3) (A_4) (A_5)

Dynamic compromise
$$C_0$$
 A_1 C_2 $Undef(C_2)$



Goal 2: Towards building secure compilation chains









- lag-based reference monitor emor
- component separation
- procedure call and return discipline (linear capabilities / linear entry points)



(linear capabilities / linear entry points)

(program rewriting, shadow call stack)



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- Support other enforcement mechanisms (back ends)
- Measure & lower overhead

Wrapping up

• Secure interoperability with lower-level code

exploring a continuum, security vs efficiency tradeoff

- Secure compilation despite dynamic compromise
 - restrict scope of undefined behavior
 - **spatially** to the component that caused it
 - temporally by treating UB as an observable trace event

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 - temporally by treating UB as an observable trace event
- We're hiring!



- PostDocs, Young Researchers, Interns, PhD students

BACKUP SLIDES

More goals of secure compilation

- Enabling source-level security reasoning
- Making the source language safer
 - memory and type safety, less/no undefined behavior
- Making it easier to express security intent
 - marking secrets, specifying security properties
- Making exploits more difficult

- CFI, CPI, stack protection, randomization, diversity









рс	tpc	mem[0]	tm0
r0	tr0	"store r0 r1"	tm1
r1	tr1	mem[2]	tm2
		 mem[3]	tm3



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			mem[3]	tm3'





software-defined, hardware-accelerated, tag-based monitoring





software monitor's decision is hardware cached 31









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- low level + fine grained: unbounded per-word metadata, checked & propagated on each instruction
- **flexible**: tags and monitor defined by software
- efficient: software decisions hardware cached
- expressive: complex policies for secure compilation
- secure and simple enough to verify security in Coq
- real: FPGA implementation on top of RISC-V
 DR ^ PER DEVER





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- protected compartments
- dynamic sealing
- heap memory safety
- code-data separation
- control-flow integrity (CFI) Evaluated
- taint tracking (<10% runtime overhead)

Verified (in Coq) [Oakland'15]

spec

[ASPLOS'15]